

Vishay High Power Products

Schottky Rectifier, 200 A



PRODUCT SUMMARY				
I _{F(AV)}	200 A			

MECHANICAL DESCRIPTION

The Generation 5 of ADD-A-PAK module combine the excellent thermal performance obtained by the usage of direct bonded copper substrate with superior mechanical ruggedness, thanks to the insertion of a solid copper baseplate at the bottom side of the device.

The Cu baseplate allow an easier mounting on the majority of heatsink with increased tolerance of surface roughness and improved thermal spread.

The Generation 5 of ADD-A-PAK module is manufactured without hard mold, eliminating in this way any possible direct stress on the leads.

The electrical terminals are secured against axial pull-out: they are fixed to the module housing via a click-stop feature already tested and proved as reliable on other Vishay HPP modules.

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation



- Guard ring for enhanced ruggedness and long term reliability
- UL pending
- Totally lead (Pb)-free, RoHS compliant
- Designed and qualified for industrial level

DESCRIPTION

The VSKDS401.. Schottky rectifier doubler module has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	200	А	
V _{RRM}		45	V	
I _{FSM}	t _p = 5 μs sine	25 000	А	
V _F	200 Apk, T _J = 125 °C	0.63	V	
TJ	Range	- 55 to 175	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VSKDS401/045P	UNITS		
Maximum DC reverse voltage	V _R	45	M		
Maximum working peak reverse voltage	V _{RWM}	40	v		



ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_C = 119 °C, rectangular waveform		200	
Maximum peak one cycle I _{FSM}	less.	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	25 000	A
	10 ms sine or 6 ms rect. pulse	V_{RRM} applied	3450		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 24 Amps, L = 1 mH		270	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		40	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	200 A	T _J = 25 °C	0.67	V
		400 A		0.87	
		200 A	T _J = 125 °C	0.63	
		400 A		0.86	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	20	mA
		T _J = 125 °C		180	
Maximum junction capacitance	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		10 300	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs
RMS insulation voltage	V _{INS}	50 Hz, circuit to base, all terminals shorted (1 s) 3500		V	

Note

 $^{(1)}\,$ Pulse width < 500 μs

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stora temperature range	ige	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance junction to case per leg	B B thic LDC operation		0.30	°C/W		
Maximum thermal resistance case to heatsink	е,	R _{thCS}	Mounting surface, smooth and greased	0.1	- C/W	
Approximate weight				110	g	
				4	oz.	
Mounting torque ± 10 %	to heatsink			5	Nm	
	busbar			4	INITI	
Case style	se style JEDEC TO-		TO-2	40AA		



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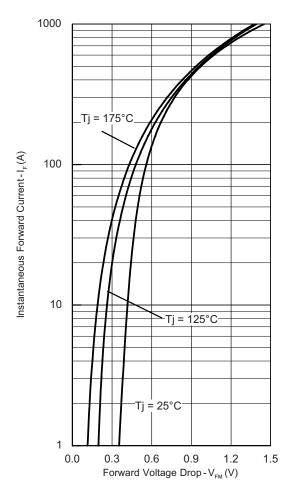


Fig. 1 - Maximum Forward Voltage Drop Characteristics

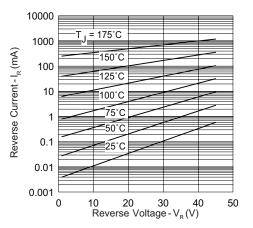


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

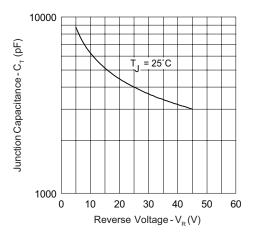


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

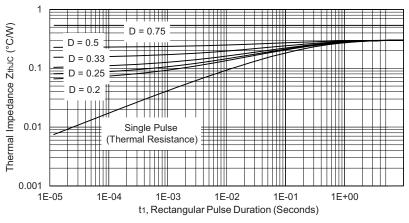
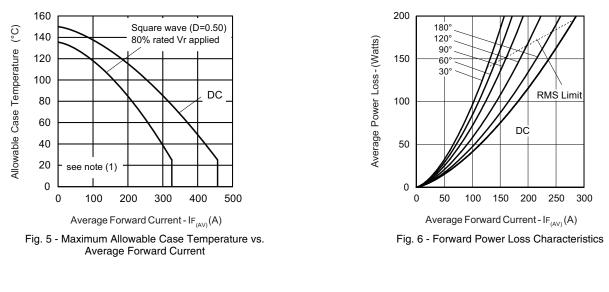
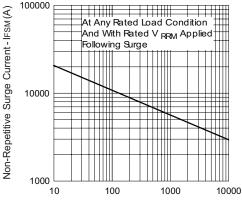


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

VSKDS401/045P

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Square Wave Pulse Duration - t_n (microsec)

Fig. 7 - Maximum Non-Repetitive Surge Current

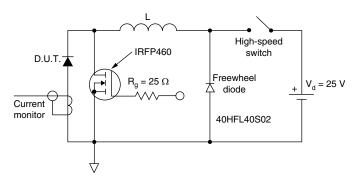


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 80 \% rated V_R$

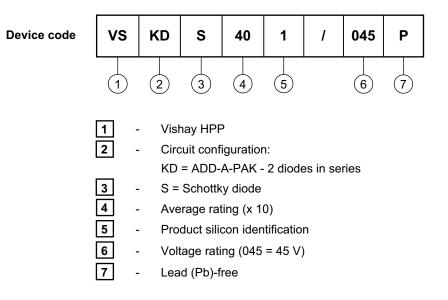
SHA



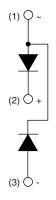
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ORDERING INFORMATION TABLE



CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95174		



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